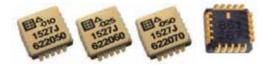
Small Bias and SF Temperature Coefficients

SILICON Designs

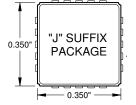
NC

ECCN 7A994

- Excellent In-Run Bias Stability
- Zero Cross Coupling by Design
- Low Power +5 VDC, 6.5 mA •
- -55 to +125°C Operation •
- ±4V Differential Analog Output •
- Internal Temperature Sensor •
- Nitrogen Damped & Hermetically Sealed
- Small J-Lead LCC-20 Ceramic Package, Mass 0.68 g
- Serialized for Traceability & RoHS Compliant



DESCRIPTION



The Model 1527 is a small, lightweight, integrated MEMS accelerometer for use in tactical-grade inertial applications requiring high repeatability, low noise, and long-term stability in -55 to +125°C environments. The Model 1527 accelerometers are fully designed and built by SDI in the USA, and each unit is individually tested, calibrated and verified for performance.

Positive Acceleration	
	0.165"

Each miniature, hermetically sealed package combines a MEMS capacitive sense element and a custom integrated circuit that includes a sense amplifier and differential output stage. Each device is marked with a serial number on its top and bottom surfaces for traceability. Documentation is supplied with each unit showing the residuals, thermal sensor model, acceleroation model, bias, scale factor, linearity, operating current, & frequency response.

ZERO (DC) O MEDIUM FREQUENCY APPLICATIONS



NAVIGATION





DESIGN SPECIFICATIONS				
PARAMETER	Typical Value/Range +/-10G	Typical Value/Range +/-25G	Typical Value/Range +/-50G	Units
Vibration Rectification, typical Random, 10-50 Hz Random, 50-200 Hz	TBA	30 100	ТВА	µg/g² rms
Velocity Random Walk	0.007	0.012	0.025	m/s√Hr½
Bias, Long Term Repeatability (1ơ)	1.25	1.50	3.0	mg
In Run Bias Stability at +1g, 2-40,000 sec. (AV Min)	12	30	60	μg
Scale Factor Long Term Repeatability (10)	300	300	300	PPM
Output White Noise	18	25	50	µg/√Hz ^½ rms
Temperature Sensor Sensitivity (IT Pin 7)	1.2 to 1.8	1.2 to 1.8	1.2 to 1.8	uA/C
Temperature Sensor Noise	0.33 RMS typ	0.33 RMS typ	0.33 RMS typ	С
Turn-On Time < 150 ppm of FS	0.5	0.5	0.5	msec
Operating Voltage	4.75 to 5.25	4.75 to 5.25	4.75 to 5.25	Volts
Input Axis Misalignment	4	4	4	mrad
Peak Vibration (Operating and Non-operating	200%	200%	200%	FS

Design specifications are inherent in the design and not tested during manufacturing

SPECIFICATIONS SUBJECT TO CHANGE WITHOUT NOTICE

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TESTED PERFORMANCE SPECIFICATIONS				
Parameter	Limit +/-10G	Limit +/-25G	LIMIT +/-50G	Units
Bias	+/- 0.5	+/- 0.5	+/- 0.5	% of FS
Bias Temperature Coefficient	±25	±15	±15	PPM of FS/ºC
Scale Factor Sensitivity, +/-0.5%	400	160	80	mV/g
Scale Factor Temperature Coefficient	±25	±25	±25	PPM/ °C
Frequency Response, DC to -3 dB, Minimum*	420	660	1050	Hz
RMS Model Residual (+/- 1g, -40, +25, +85°C)	30	25	25	PPM of FS

*Frequency Response reported as performance once soldered. Simulating Frequency Response with the DV pin will report lower values than actual performance once soldered.

MAX OPERATING LIMITS				
Parameter	Minimum	Μαχιμυμ	Units	
Differential Output	-4.0	+4.0	Volts	
Operating Voltage	4.75	5.25	Volts	
Quiescent Operating Current at +5V		6.5	mA	
Operating / Storage Temperature	-55	+125	0°C	
Applied Voltage on Digital Pins	-0.5	5.5	Volts	
Mechanical Shock (0.1 ms)		5,000	g-peak	
Peak Vibration (Operating and Non-operating)		200	% of FS	

FS = Full Scale = 4V absolute differential output = 4000 mV

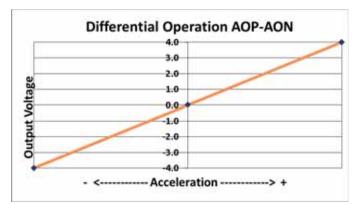
NOTICE: Stresses greater than those listed above may cause permanent damage to the device. These are maximum stress ratings only. Functional operation of the device at or above these conditions is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability and lifespan.

OPERATION

The model 1527 sensitive axis is perpendicular to the bottom of the package, with positive acceleration resulting from a positive force pushing on the bottom of the package. The seismic center is located on a centerline through the dual sense elements and halfway between them.

The Model 1527 produces a differential +/-4 volts output voltage, the value of which varies with acceleration as shown in figure 1. The seismic center is located on a centerline through the dual sense elements halfway between them. Any errors due to rotation about this point are effectively cancelled by the internal electronics

Two reference voltages, +5.0 and +2.5 volts (nominal), are required; scale factor is ratiometric to the +5.0 volt reference voltage relative to GND, and both outputs at zero acceleration are nominally 80 mV below the +2.5 volt input.

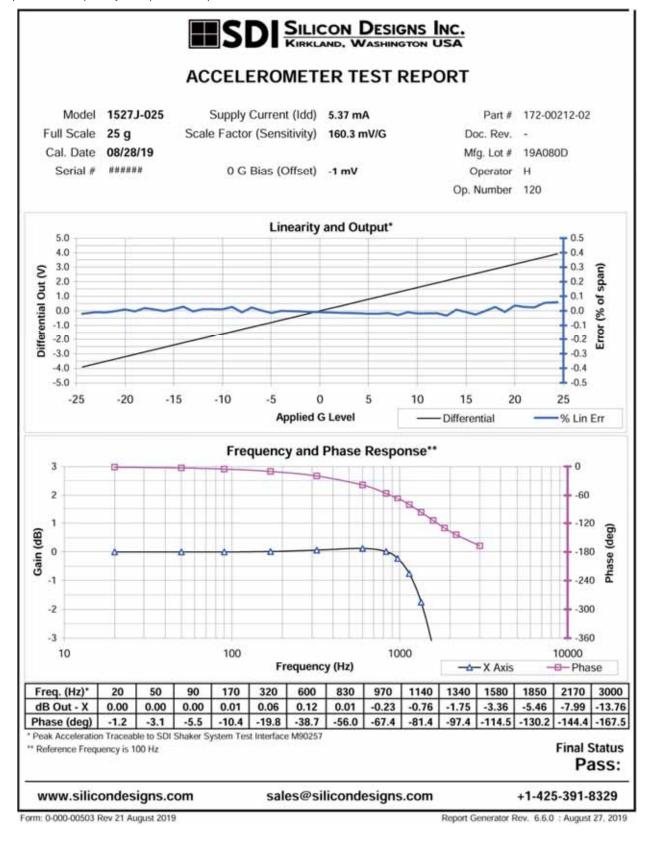


The 1527 should not be used in Single Ended mode



SAMPLE OF INCLUDED TEST REPORT

Every 1527 accelerometer is accompanied by a test report featuring the measured bias, scale factor, linearity, output, and frequency and phase response.



SIGNAL DESCRIPTIONS

<u>VDD and GND (power)</u>: Pins (14) and (19) respectively. Power (+5 Volts DC) and ground.

<u>AOP and AON (output)</u>: Pins 12 and 16 respectively. Analog output voltages proportional to acceleration. The AOP voltage increases (AON decreases) with positive acceleration; at zero acceleration both outputs are nominally equal to the +2.5 volt reference. The device experiences positive (+1g) acceleration with its lid facing up in the earth's gravitational field. Use of differential mode is required for both low noise and high accuracy operation. Voltages can be measured ratio-metrically to VR for good repeatability without requiring a separate precision reference voltage for an A/D.

<u>DV (input)</u>: Pin 4. Deflection Voltage. Connect to the 2.5 Volt pin for best repeatability. A test input that applies an electrostatic force to the sense element, simulating a positive acceleration. The nominal voltage at this pin is ½ VDD. DV voltages higher than required to bring the output to positive full scale may cause device damage to 2g, 5g, and 10g devices. See app note.

<u>VR (input and Ref GND)</u>: Pin 3. Voltage Reference. Tie VR to a good reference (not directly to VDD) for best scale factor repeatability. A 0.1µF bypass capacitor is recommended at this pin. VR current is less than 100 µA. Ref GND can be connected to pin 19. However, to minimize 1/f noise, connect pin 20 separately to the ground of the voltage reference ground pin.

<u>2.5 Volt (input)</u>: Pin 17. Sets output common-mode value. Tie to a resistive voltage divider from +5 volts. A 0.1μ F bypass capacitor is recommended at this pin. VR current is less than 50 μ A.

<u>IT (output)</u>: Pin 7. Uncalibrated temperature dependent current source. Tie to V_{DD} if not used.

<u>CLKIN</u>: Pin 8. Optional external clock input, leave unconnected or contact SDI for details on this feature.

<u>Special Use Pins</u>: Pins 9 and 11 should be tied to V_{DD} ; Pins 2,5,6 and 18 to GND; Pins 1,7,10,13, and 15 are reserved and should remain unused. Contact SDI for possible special use of these pins.

Ref GND GND (gud) 20 19 18 DV (gnd) (gnd) 17 +2.5V 1527 (gnd) 16 AON TOP VIEW 15 IT [14] VDD CLKIN 13 10 11 12 (ppv) AOP (ppv)

INTERNAL CLOCK

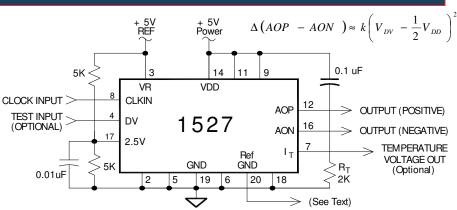
The model 1527 contains an internal clock than runs at approximately 900 KHz. The internal clock is powered by VDD. Like other synchronous sensors, it is subject to clock "lock-in" with other accelerometers driven by the same VDD. To avoid possible lock-in and small bias jumps, it is recommended that the VDD power to each accelerometer be supplied by separately buffered sources or filtered from a common well bypassed source by a LC filter with a minimum of 20 dB loss at 800 KHz. Alternatively, multiple accelerometers can be driven by the same external clock with a frequency in the range of 0.5 to 1 MHz. Contact SDI for more information on using an external clock.

III SDI

RECOMMENDED CONNECTIONS

DEFLECTION VOLTAGE (DV) TEST

<u>INPUT</u>: This test input applies an electrostatic force to the sense element, simulating a positive acceleration of up to +10g. It has a nominal input impedance of 32 k Ω and a nominal open circuit voltage of $\frac{1}{2}$ VDD. For best accuracy during normal operation, this input should be left unconnected or connected to a voltage source equal to $\frac{1}{2}$ of the VDD supply.



The change in differential output voltage (AOP - AON) is proportional to the square of the difference between the voltage applied to the DV input (V_{DV}) and $\frac{1}{2}$ V_{DD}. Only positive shifts in the output voltage may be generated by applying voltage to the DV input. When voltage is applied to the DV input on 2g, 5g, and 10g devices, it should be applied gradually. The application of DV voltages greater than required to bring the output to positive full scale may cause device damage. The proportionality constant (k) varies for each device and is not trimmed to a value.

<u>ESD and LATCH-UP CONSIDERATIONS</u>: The model 1527 accelerometer is a CMOS device subject to damage from large electrostatic discharges. Diode protection is provided on the inputs and outputs, and it is not easily damaged, but care should be exercised during handling. However, individuals and tools should be grounded before coming in contact with the device. Although the 1527 is resistant to latch-up, inserting a 1527 into or removing it from a powered socket may cause damage.

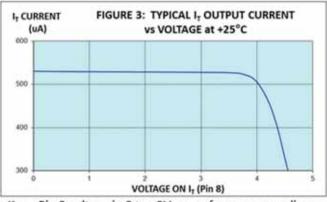
INTERNAL TEMPERATURE SENSING

The model 1527 accelerometer outputs a temperature dependent current source on pin 8. This signal is useful for measuring the internal temperature of the accelerometer so that any previously characterized bias and scale factor temperature dependence, for a particular accelerometer, can be corrected. The nominal output current at 25°C is \approx 500 (±200) µA and the nominal sensitivity is 1.5 (±0.5) µA/°C. With a single resistor RT = 2K between IT (pin 8) and GND the output voltage VT will vary between +0.76 and +1.3 volts from -55 to +125°C, which equates to a sensitivity of ≈+3 mV/°C.

	MIN	TYP	MAX	UNITS
Temperature Sensor White Noise (1-1000 Hz):	0.25	0.25	0.25	°C rms

If a greater voltage change versus temperature or lower signal source impedance is needed, add the amplifier as shown on the right side in Figure 2. With offset voltage VoFF = -5V, gain resistor R_G = 15.0K and offset resistor RoFF = 7.32K, the output voltage VT will vary between +4.5 and +0.5 Volts from -55 to +125°C, which equates to a sensitivity of \approx -29 mV/°C. Figure 3 shows the voltage compliance of the temperature dependent current source [IT] at room temperature. The voltage at pin 8 must be kept in the 0 to +3V range in order to achieve proper temperature readings.

$$V_{T} \approx R_{T} [(500\,\mu A) + [(1.5\,\mu A)(T-25)]] \qquad \frac{\Delta V_{T}}{\Delta T} = R_{T} (1.5\,\mu A)$$
$$R_{OFF} = \frac{-V_{OFF}}{\left(\frac{V_{T}}{R_{G}}\right) + (500\,\mu A) + [(1.5\,\mu A)(T-25)]}$$
$$V_{T} \approx -R_{G} \left[\frac{V_{OFF}}{R_{OFF}} + (500\,\mu A) + [(1.5\,\mu A)(T-25)]\right]$$
$$\frac{\Delta V}{\Delta T} = -R_{G} (1.5\,\mu A) \qquad R_{G} = \frac{-\Delta V_{T}}{(1.5\,\mu A)(\Delta T)}$$



Keep Pin 8 voltage in 0 to +3V range for proper readings.



SOLDERING RECOMMENDATIONS

RoHS Compliance: The model 1527 does not contain elemental lead and is RoHS compliant.

Pre-Tinning of Accelerometer Leads is Recommended: To prevent gold migration embrittlement of the solder joints, it is best to pre-tin the accelerometer leads.

Soldering: Solder reflow should not exceed 239°C, exceeding this temperature may result in permanent damage.

J-LCC Solder Contact Plating Information: The plating composition and thickness for the solder pads and castellations on the J-Lead package top layer are 100 to 225 micro inches thick of 99.7% gold (Au) over 80 to 350 micro inches thick of electroplated nickel (Ni).

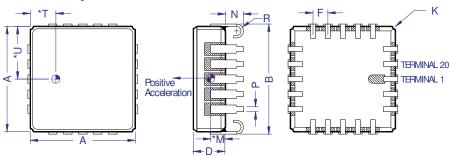
These dimensions are recommendations only and may or may not be optimal for your soldering process.

Do not use ultrasonic cleaners.

Ultrasonic cleaning voids the warranty and may break internal wire bonds.

PACKAGE DIMENSIONS

- 1. *Dimensions "M," "T," and "U" locate sensing element's center of mass.
- 2. Lid is electrically tied to terminal 19 (GND).
- 3. Controlling dimension: Inch.
- 4. Terminals are plated with 60 micro inches min gold over 80 micro inches min nickel. This plating specification does not apply to the Pin-1 identifier mark on the bottom of the J-lead package version.
- Package: 90% min alumina (black), lid: solder sealed Kovar. 5.



	Inches		Millim	eters
Dim	Min	Max	Min	Max
А	0.342	0.358	8.69	9.09
В	0.346	0.378	8.79	9.60
D	0.095	0.115	2.41	2.92
F	0.050	0.050 BSC		BSC
K	0.010 R TYP		0.25 R TYP	
* M	0.066 TYP		1.68 TYP	
Ν	0.050	0.070	1.27	1.78
Р	0.017	0.017 TYP		TYP
R	0.023 R TYP		0.58 F	R TYP
* T	0.085 TYP		2.16	TYP
* U	0.175 TYP		4.45	TYP
Mass 0.68 g				

COMPANION ACCESSORY

K

The SDI Model 2227 Q-Module combines a high-temperature, open printed circuit board (PCB) specifically designed to give the high stability required for inertial applications and SDI's Model 1527 Tactical-Grade Inertial Navigation MEMS Accelerometer. It is a low-mass, low-power module, electrically and mechanically compatible with fixtures and equipment designed for industrystandard guartz accelerometers and intended for use in inertial and tilt applications requiring zero to medium frequency response excellent longterm bias and scale factor repeatability, and low noise.





株式会社

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